Sample Acknowledgement

| Customer Name | | | | | |
|--------------------|------------------|-----------------------|---------------------|----------------------|-------------------|
| Product Name | Solar panel k | (SEM-270X108n | nm | | |
| Product Number | | | | | |
| Use environment of | utdoor | | | | |
| Date 2025-8-15 | | | | | |
| | The purpose of e | establishing this sta | ndard is to provide | both manufacturers | s and users with |
| | A clear standard | d, and acceptance լ | orocedures accordi | ng to this standard; | |
| | Some contents | not included in the | standard can be su | pplemented after n | nutual agreement. |
| | Add corrections. | | | | |
| | | | | | |
| | | | | confirm | |
| | | | | | |
| | | | | | |
| | | | | | |
| | | | | sign: | |
| | | | | date: | |

Note: After you review and approve the Sample Acknowledgement and sign in the "Customer Acknowledgement" column, our company

It is deemed that your company accepts all the contents of this "Sample Acknowledgement Letter".

1. Scope of use

Solar lamps, power supplies, power generation systems and other solar application products.

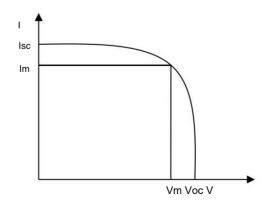
- 2. Performance test conditions and methods:
- ① Standard test conditions: AM1.5, E=1000W/m², T=25°C
- 2 Test method: Using solar simulation test equipment, test voltage and current at a light intensity of 38,000.

3. Performance specifications

| Name | Parameters | Remark |
|--------------------------------|---|------------------|
| Packaging | Frosted PET Laminated Frosted | Apply black tape |
| Material | PET+EVA | |
| battery cells | single crystal silicon | |
| Voltage | 6V | |
| Current | 1.5A | Error ±5MA |
| base material | PCB | |
| Base plate shape/size | 270X108mm | Error ±0.1mm |
| Base plate thickness | 1.0=mm | Error ±0.20mm |
| Component thickness | 2.2mm | Error ±0.20mm |
| Working environment conditions | Temperature range -10+70 ℃ humidity 45—95%RH | |

4. Appearance requirements

- A. The battery cell requires single crystal silicon wafers
- B. The film arrangement must be neat;
- C. The surface of the finished product must be smooth, without scratches, bumps, deformation, dirt, delamination, whitening, broken lines, bubbles, gaps, etc. Good; slight color difference is allowed
- D. The soldering ribbon cannot be too long to connect the positive and negative poles of the S+/S- pads together
- 5. The schematic diagram of the I-V curve of the solar cell module is as follows:



In the figure: Isc is the output current when the load is 0, that is, the short-circuit current, and the output power is 0 at this time.

Voc is the output voltage when the load is open, also known as the open-circuit voltage. At this point, the output power is zero.

As the load on a solar cell module changes, the operating point and output power also change. At a certain point, the output power reaches its maximum. The output voltage and current at this point are the maximum operating voltage (Vm) and maximum operating current (Im). Different solar cell materials may have slight variations.

6. Storage Conditions: Store at room temperature.

Please conduct QC testing according to the above standards. If any quality issues arise within the test period, please notify us in writing.

Test Report

| Model | Solar panel KSEM-270X108mm |
|-------------------------------------|----------------------------|
| Dimension | 270*108 |
| Photoelectric conversion efficiency | 19.8% |
| Maximum power | 9W |
| Operating voltage | 6V |
| voltage | |
| Open-circuit voltage | 6.2V |
| Short-circuit current | 1.5A |

Note:

- 1. The test size parameter error is ±0.1mm
- 2. The voltage parameter error of the electrical indicator is 2V.
- 3. The current parameter error of the electrical indicator is $\pm 5 \text{mA}$.

|--|

