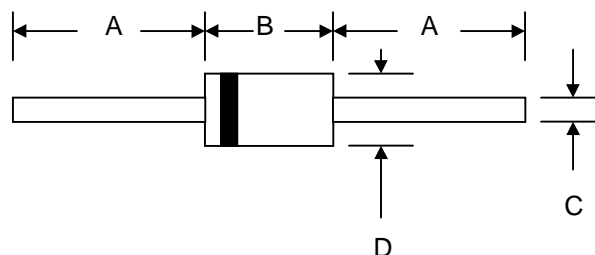


Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability

Mechanical Data

- Case: DO-15, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.40 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- **Lead Free: For RoHS / Lead Free Version**



DO-15		
Dim	Min	Max
A	24.5	—
B	5.50	7.62
C	0.60	0.80
D	2.60	3.60
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	FR201	FR202	FR203	FR204	FR205	FR206	FR207	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	50	100	200	400	600	800	1000	V
RMS Reverse Voltage	VR(RMS)	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1) @TA = 75°C	IO	2.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	60							A
Forward Voltage @IF = 2.0A	VFM	1.3							V
Peak Reverse Current At Rated DC Blocking Voltage @TA = 25°C @TA = 100°C	IRM	5.0 100							μA
Reverse Recovery Time (Note 2)	trr	150				250	500		nS
Typical Junction Capacitance (Note 3)	Cj	30							pF
Operating Temperature Range	Tj	-65 to +150							°C
Storage Temperature Range	TSTG	-65 to +150							°C

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case

2. Measured with I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A. See figure 5.

3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

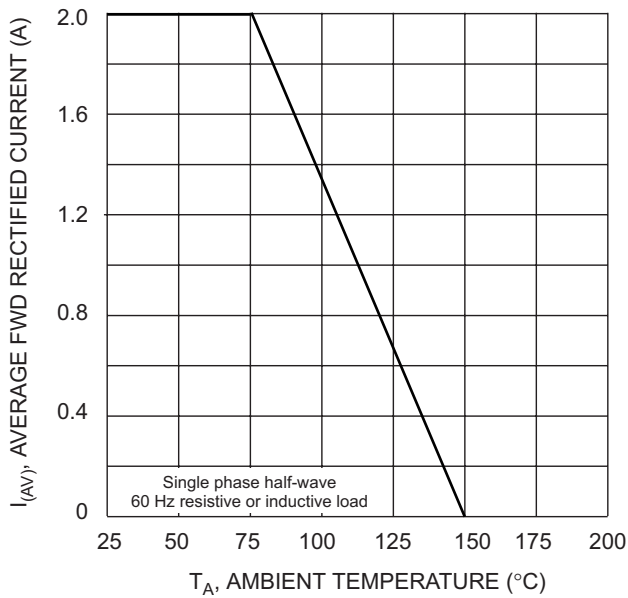


Fig. 1 Forward Derating Curve

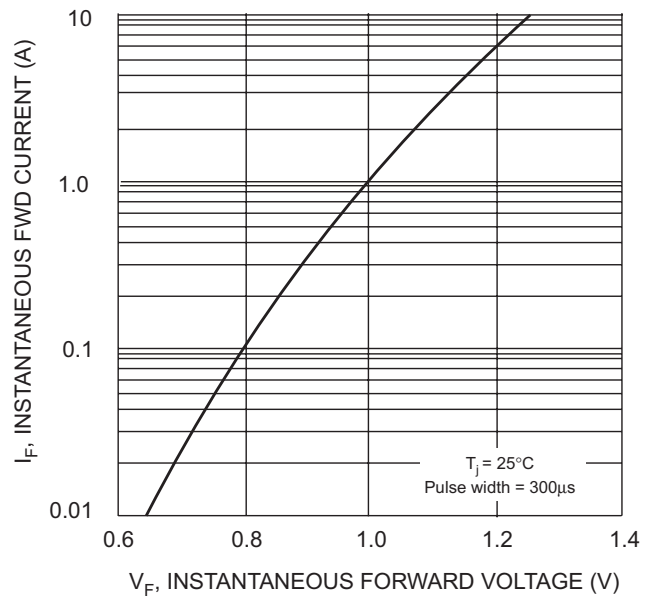


Fig. 2 Typical Forward Characteristics

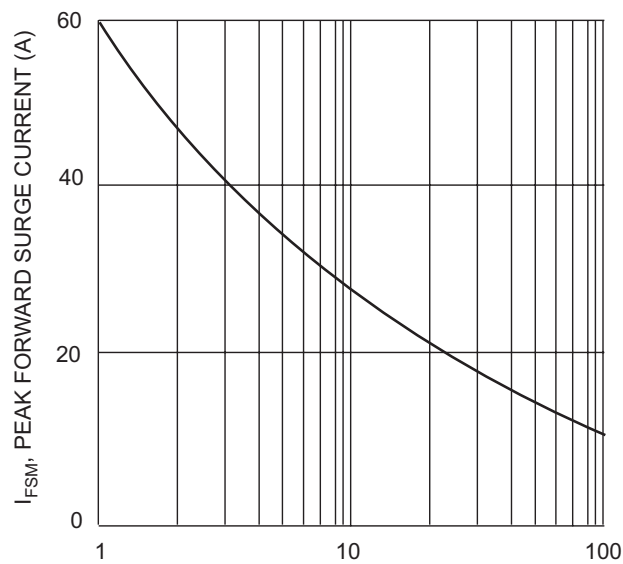


Fig. 3 Peak Forward Surge Current

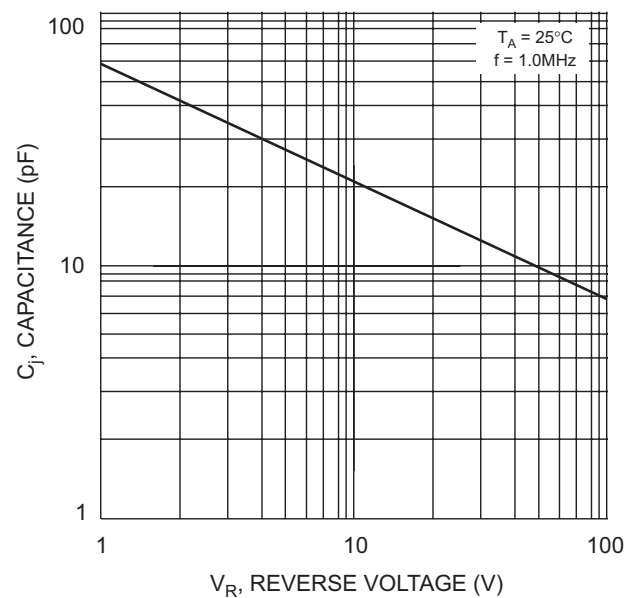
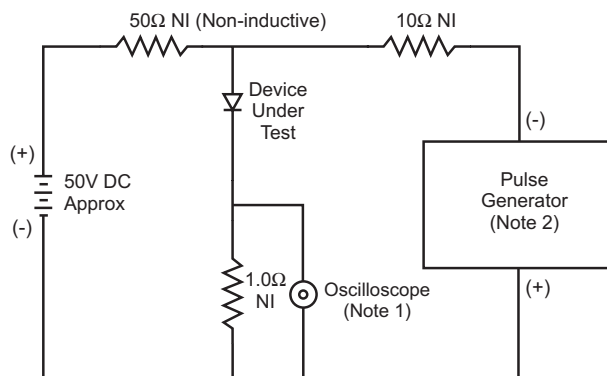


Fig. 4 Typical Junction Capacitance



Notes:

1. Rise Time = 7.0ns max. Input Impedance = 1.0M Ω , 22pF.
2. Rise Time = 10ns max. Input Impedance = 50 Ω .

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

